



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Inventor Mark A. Helm et al.
Assignee Micron Technology, Inc.
Group Art Unit 2826
Examiner Fetsum Abraham
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Title: Gated Semiconductor Assemblies and Methods of Forming Gated Semiconductor Assemblies

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TERMINAL DISCLAIMER

I, James E. Lake, residing at Liberty Lake, Washington, represent that I am the attorney of record for Micron Technology, Inc., a corporation of the state of Delaware. Micron Technology, Inc. is the owner of all right, title and interest of this U.S. Patent Application Serial No. 10/769,573, filed January 30, 2004, as evidenced by an assignment recorded April 2, 1998, Reel 9082, Frames 0702-0707 in the U.S. Patent and Trademark Office.

Micron Technology, Inc. is also the owner of all rights, title and interest in U.S. Patent No. 6,756,634, as evidenced by an assignment recorded April 2, 1998, at Reel 9082, Frames 0702-0707, in the U.S. Patent and Trademark Office.

The evidentiary documents have been reviewed, and I certify that, to the best of assignee's knowledge and belief, title is in the assignee seeking to take this action.